PATENT 1 2 158-956 Technology Center 2100 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE 3 4 5 In Re Application of: Matthew J. Amatangelo, et al. ٠6 8 Serial No.: 09/888,474 Group Art Unit: 2123 9 10 Filed: June 25, 2001 Examiner: Unknown 11 12 FOR: Method of Utilizing Timing Models to Provide Data for 13 14 Static Timing Analysis of **Electronic Circuits** 15 16 17 Commissioner for Patents 18 Washington, D.C. 20231 19 20 INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97 _21 22 Dear Sir: 23 24 The following items are enclosed for filing in the above-identified patent application: 25 26 Form PTO/SB/08A, along with copies of listed references <u>X</u> Stamped, self-addressed return postcard to be returned 27 X 28 29 If there is any fee due please charge such fee to Deposit Account No. 19-1453 (Our File 30 No. 158-956). 31 Respectfully submitted, 32 SHAFFER/& CULBERTSON, L.L.P. 33 34 --35 By: 36 Russell D. Culbertson, Reg. No. 32,124 37 J. Nevin Shaffer, Jr., Reg. No. 29,858 38 1250 Capital of Texas Hwy. South 39 Building I, Suite 360 40 Austin, Texas 78746 41 512-327-8932 42 ATTORNEYS FOR APPLICANT 43 44 45 46 47 48 49 50 pc: Ms. Bev Carter CERTIFICATE OF MAILING

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Sheet 1 of 2

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Application Number	09/888,474	
Filing Date	June 25, 2001.	
First Named Inventor	Matthew J. Amatangelo	
Group Art Unit		
Examiner Name		
Attorney Docket Number	AUS9-2001-0049-US1	

				U.S. PATENT DOCU	JMENTS	
Examiner Initials*	Cite No.1	U.S. Patent Number	Document Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		5,651,012		Jones	07/22/1997	
		5,790,830		Segal	08/04/1998	
		6,083,273		Takeuchi	07/04/2000	
		6,158,022		Avidan	12/05/2000	
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